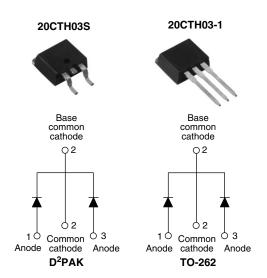


### Vishay High Power Products

### Hyperfast Rectifier, 2 x 10 A FRED Pt<sup>TM</sup>



PRODUCT SUMMARY				
t <sub>rr</sub> (maximum)	35 ns			
I <sub>F(AV)</sub>	2 x 10 A			
$V_{R}$	300 V			

#### **FEATURES**

- · Hyperfast recovery time
- Low forward voltage drop
- · Low leakage current
- 175 °C operating junction temperature
- Designed and qualified for industrial level

#### **DESCRIPTION/APPLICATIONS**

300 V series are the state of the art hyperfast recovery rectifiers designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness and reliability characteristics.

These devices are intended for use in the output rectification stage of SMPS, UPS, dc-to-dc converters as well as freewheeling diode in low voltage inverters and chopper motor drives.

Their extremely optimized stored charge and low recovery current minimize the switching losses and reduce over dissipation in the switching element and snubbers.

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	MAX.	UNITS
Peak repetitive reverse voltage		$V_{RRM}$		300	V
Average rectified forward current — — —	per diode	I <sub>E(AV)</sub>	T <sub>C</sub> = 160 °C	10	
	per device			20	Α
Non-repetitive peak surge current		I <sub>FSM</sub>	T <sub>J</sub> = 25 °C	120	
Operating junction and storage temperatures		T <sub>J</sub> , T <sub>Stg</sub>		- 65 to 175	°C

<b>ELECTRICAL SPECIFICATIONS</b> (T <sub>J</sub> = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V <sub>BR</sub> , V <sub>R</sub>	Ι <sub>R</sub> = 100 μΑ	300	-	-	
Forward voltage V <sub>F</sub>	V	I <sub>F</sub> = 10 A	-	1.05	1.25	V
	I <sub>F</sub> = 10 A, T <sub>J</sub> = 125 °C	-	0.85	0.95		
Develop lockers surrent		$V_R = V_R$ rated	-	=	20	
Reverse leakage current I <sub>R</sub>	I R	$T_J = 125 ^{\circ}\text{C},  V_R = V_R  \text{rated}$	-	6	200	μΑ
Junction capacitance	C <sub>T</sub>	V <sub>R</sub> = 300 V	-	30	-	pF
Series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body	ı	8	-	nH

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## 20CTH03S/20CTH03-1

Vishay High Power Products

# Hyperfast Rectifier, $2 \times 10 \text{ A FRED Pt}^{TM}$



<b>DYNAMIC RECOVERY CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1.0 \text{ A}, dI_F/dt =$	50 A/μs, V <sub>R</sub> = 30 V	-	-	- 35	
Poverse recovery time		$I_F = 1.0 \text{ A}, dI_F/dt = 100 \text{ A/}\mu\text{s}, V_R = 30 \text{ V}$		-	-	30	
Reverse recovery time	t <sub>rr</sub>	T <sub>J</sub> = 25 °C		-	31	-	UNITS ns A nC
		T <sub>J</sub> = 125 °C		-	42	-	
Peak recovery current		T <sub>J</sub> = 25 °C	$I_F = 10 \text{ A}$	-	2.4	-	
	I <sub>RRM</sub>	T <sub>J</sub> = 125 °C	$dI_F/dt = 200 \text{ A/}\mu\text{s}$ $V_B = 200 \text{ V}$	-	5.6	-	A
Reverse recovery charge	0	T <sub>J</sub> = 25 °C		-	36	-	0
	Q <sub>rr</sub>	T <sub>J</sub> = 125 °C		-	120	-	nC nC

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T <sub>J</sub> , T <sub>Stg</sub>		- 65	-	175	°C
Thermal resistance, junction to case per diode	R <sub>thJC</sub>		-	-	1.5	°C/W
Weight			-	2.0	-	g
Weight		-	0.07	-	OZ.	
Mounting torque			6.0 (5.0)	-	12 (10)	kgf · cm (lbf · in)
Madiandada		Case style D <sup>2</sup> PAK	20CTH03S			•
Marking device		Case style TO-262	20CTH03-1			

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# Hyperfast Rectifier, 2 x 10 A FRED Pt<sup>TM</sup>

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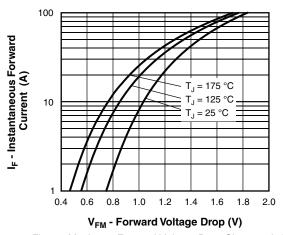


Fig. 1 - Maximum Forward Voltage Drop Characteristics

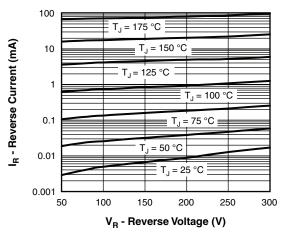


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

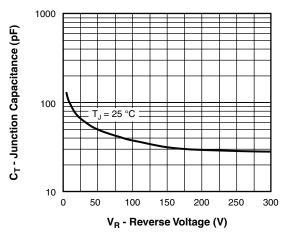


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

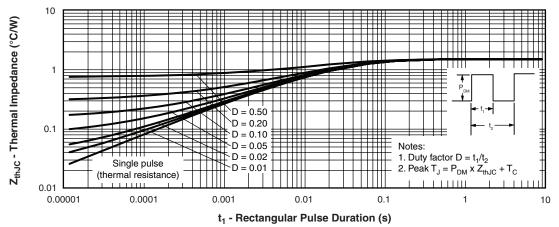
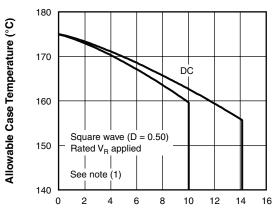


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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# Hyperfast Rectifier, 2 x 10 A FRED Pt<sup>TM</sup>





 $I_{F(AV)}$  - Average Forward Current (A)

Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

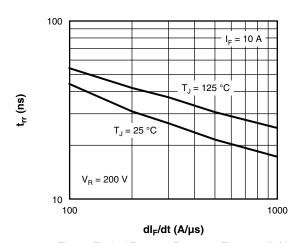


Fig. 7 - Typical Reverse Recovery Time vs. dI<sub>F</sub>/dt

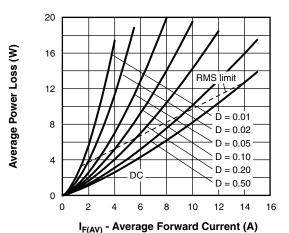


Fig. 6 - Forward Power Loss Characteristics

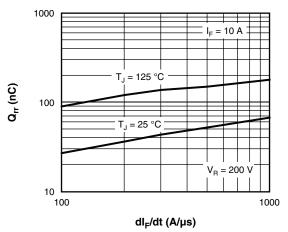


Fig. 8 - Typical Stored Charge vs. dl<sub>F</sub>/dt

#### Note

 $\begin{array}{ll} \text{(1)} \;\; \text{Formula used:} \; T_C = T_J - (Pd + Pd_{REV}) \; x \; R_{thJC}; \\ Pd = \text{Forward power loss} = I_{F(AV)} \; x \; V_{FM} \; \text{at} \; (I_{F(AV)}/D) \; (\text{see fig. 6}); \\ Pd_{REV} = \text{Inverse power loss} = V_{R1} \; x \; I_R \; (1 - D); \; I_R \; \text{at} \; V_{R1} = \text{Rated} \; V_R \\ \end{array}$ 



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## Vishay High Power Products

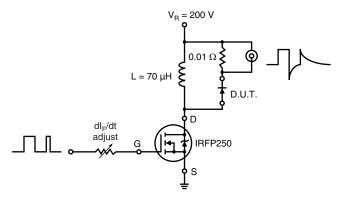
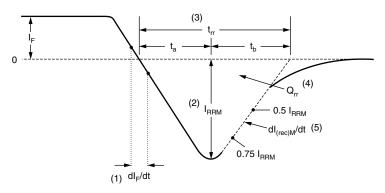


Fig. 9 - Reverse Recovery Parameter Test Circuit



- (1) dl<sub>F</sub>/dt rate of change of current through zero crossing
- (2)  $I_{RRM}$  peak reverse recovery current
- (3)  $\rm t_{rr}$  reverse recovery time measured from zero crossing point of negative going  $\rm I_{r}$  to point where a line passing through 0.75  $\rm I_{RRM}$  and 0.50  $\rm I_{RRM}$  extrapolated to zero current.
- (4)  $\mathbf{Q}_{\rm rr}$  area under curve defined by  $\mathbf{t}_{\rm rr}$  and  $\mathbf{I}_{\rm RRM}$

$$Q_{rr} = \frac{t_{rr} \times I_{RRM}}{2}$$

(5) dI<sub>(rec)M</sub>/dt - peak rate of change of current during t<sub>b</sub> portion of t<sub>rr</sub>

Fig. 10 - Reverse Recovery Waveform and Definitions

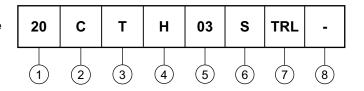
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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Current rating (20 A)

2 - C = Common cathode

3 - T = TO-220, D<sup>2</sup>PAK

4 - H = Hyperfast recovery

Voltage rating (03 = 300 V)

**6** - • S =  $D^2PAK$ 

• -1 = TO-262

7 - • None = Tube (50 pieces)

• TRL = Tape and reel (left oriented, for D<sup>2</sup>PAK package)

• TRR = Tape and reel (right oriented, for D<sup>2</sup>PAK package)

8 - • None = Standard production

• PbF = Lead (Pb)-free

LINKS TO RELATED DOCUMENTS				
Dimensions http://www.vishay.com/doc?95014				
Part marking information	http://www.vishay.com/doc?95008			
Packaging information	http://www.vishay.com/doc?95032			

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